

COURSE: ENGINEERING 17 SEMESTER: SPRING 1998  
BASIC ELECTRIC CIRCUIT ANALYSIS

INSTRUCTOR: J. Oxley  
OFFICE: FE-4D  
PHONE: 638-3641 ext. 215

OFFICE HOURS: T 11:00 - 12:00 am  
W 1:00 - 2:00 pm (Tutorial Centre)  
Th 11:00 - 12:00 am  
or by appointment

TEXT: Basic Electric Circuit Analysis, Fifth Ed., Johnson, Hilburn, Johnson, and Scott

OBJECTIVES: To teach students DC and AC circuit analysis techniques, network theorems, and power calculation. To give hands on laboratory experience in using electrical test and analysis equipment. To give students practice in written communication of experimental results.

EXAMS: There will be 4 tests throughout the semester, one on each of the major topic areas; plus a comprehensive final

GRADING: 70% - Highest 4 exams scores (the lowest of the five test scores is dropped)  
10% - Homework  
20% - Laboratory reports  
(Late labs - 10% per week)

Scale: 89% A  
77% B  
65% C  
50% D

ATTENDANCE: In accordance with KRCC policy, attendance is mandatory. A student may be dropped if more than six absences have been recorded unless special arrangements have been made. Course withdrawals, however, are ultimately the responsibility of the student. Any missed labs must be made up or a zero will be recorded for that report. Missed exams without prior approval will be counted as a zero. (Remember that the low test grade is dropped).

DROP DATE: Friday, March 13

HOLIDAYS: Monday, Jan. 19 Martin Luther King Day  
Friday, Feb. 13 Lincoln's Birthday  
Monday, Feb. 16 Washington's Birthday  
Monday to Friday, April 6 - 10 Spring Recess

FINAL: Monday, May 18 10:30 - 12:30 PM